

- U21: (1636) {scheme or method} with 12
- U22: (354) 14 with schedule with 12
- U24: (32) 12 with (override or adminster)
- U25: (127) 12 with (override or adminis-)
- U26: (33231) (override or administ-) with {change or s
- U27: (63) 135 and 126
- U29: (5) {cost with job} with 12
- U17: (23) formula with 12
- U28: (574) 12 with 14
- U29: (168) @ad="19980501" and 128
- U30: (0) 12 with 114
- U31: (5) 12 same 114
- U32: (1236) share with fair
- U33: (12) 132 with 12
- U34: (86) share with fair with queue
- U35: (40) 132 and 130 and 131
- U36: (63) 127 and 12
- U37: (16) @ad="19980501" and 136
- U38: (25) @ad="19980501" and 119
- U39: (835) @ad="19980501" and 121
- U40: (5) 128 and subqueue

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Failed
#17 same 13
#18 with (override or administer) with #14 near5 (change or
#19 (override or administer) with #14 near6 (change or se

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[0x] 12-FCMUBUSPAT #FE 0FA 0P DERIVANT BY JCK
[0x] [C= *]
[0x] = "198855301" and 131

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1	US 20020085933 A1	20020711	10	CONGESTION MANAGEMENT IN A MULTI-PORT SHARED MEMORY	370/236	370/414		GIBSON, NATALIE et al.																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		</																																																																																																																																																																																																																																																																																																							

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U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	T	S	S	Ima
1	US 6620001 A	19991207	26	Multiple priority accelerated graphics port service priority queue implemented with ordered	710/244	710/54		Lerson; Douglas A.						US 6
2	US 3872934 A	19990216	11	Service priority queue implemented with ordered	710/212	709/219; 719/100		Williams; Syron A.						US 9
3	US 5521916 A	19960526	16	Implementation of selective pushout for space priorities	570/414			Choudhury; Ashjit K. et al.						US 5
4	US 5129443 A	19940816	21	System and method for reducing storage channels in	710/6			Karger; Paul E. et al.						US 5
5	US 5247677 A	19920921	11	Stochastic priority-based task scheduler	718/103			Selland; Robert V. et al.						US 5
6	US 5274099 A	19930629	22	Circuitry and method for fair queuing and servicing	370/412	370/429; 370/463		Corkalis; Charles M. et al.						US 5
7	US 5752026 A	19980512	7	Object-oriented query mechanism	707/103n	707/1		Ellacott; Bruce Arthur						US 5
8	US 5213434 A	19940517	33	Congestion control for cell networks	370/231	370/236; 370/397		Bertini; Lionel A. et al.						US 5
9	US 4757529 A	19880712	9	Call distribution arrangement	379/266.02	379/354; 379/412		Glaes; Martin J. et al.						US 4

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